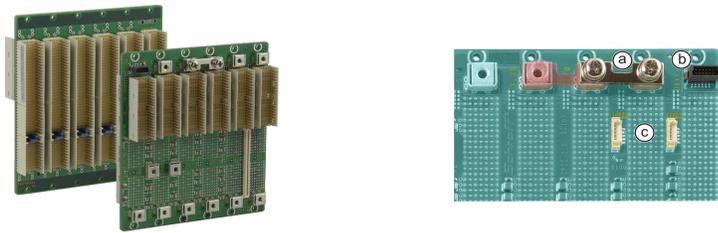


23006-405 CPCI BACKPLANE, SYSTEM SLOT LEFT, 3 U, 5 SLOT, 32-BIT, 3.3 V V/I/O



KEY FEATURES

In accordance with: PICMG 2.0 R3.0 CompactPCI Core specification, PICMG 2.1 R2.0 Hot Swap specification, PICMG 2.9 R1.0 System Management Bus specification, PICMG 2.10 R1.0 Keying specification

Digital GND can be connected to/isolated from chassis-GND by means of screws

V(I/O) adjustable to +3.3 V or +5 V

Infeed of supply voltages via powerbugs (ring tag M4), FASTONS

Outer layers act as GND surfaces

Utility plug for status signals (SMCQ)

Multiple backplanes can be placed together without loss of slots

Outstanding high-frequency noise suppression and very high MTBF values due to ceramic capacitors

Intelligent platform management bus (IPMB) connector to PICMG 2.9

PRODUCT ATTRIBUTES

Product Type: Backplane

Number of Slots: 5

Rack Height: 3 U

Backplane Type: CompactPCI, 32-bit

System Slot: Left

Slot Width: 4 HP

Voltage I/O: 3.3 V

Connector Type: HM 2.0

Power Connector Type: Power Bugs for M4 Cable Lugs

Utility Connector Type: SMCQ, 12-pin

Width: 100.6 mm

ADDITIONAL PRODUCT DETAILS

The nVent SCHROFF CompactPCI Backplanes have best in class signal integrity and MTBF values due to SMD components, ceramics capacitors and press fit connectors.

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